

FIG. 1

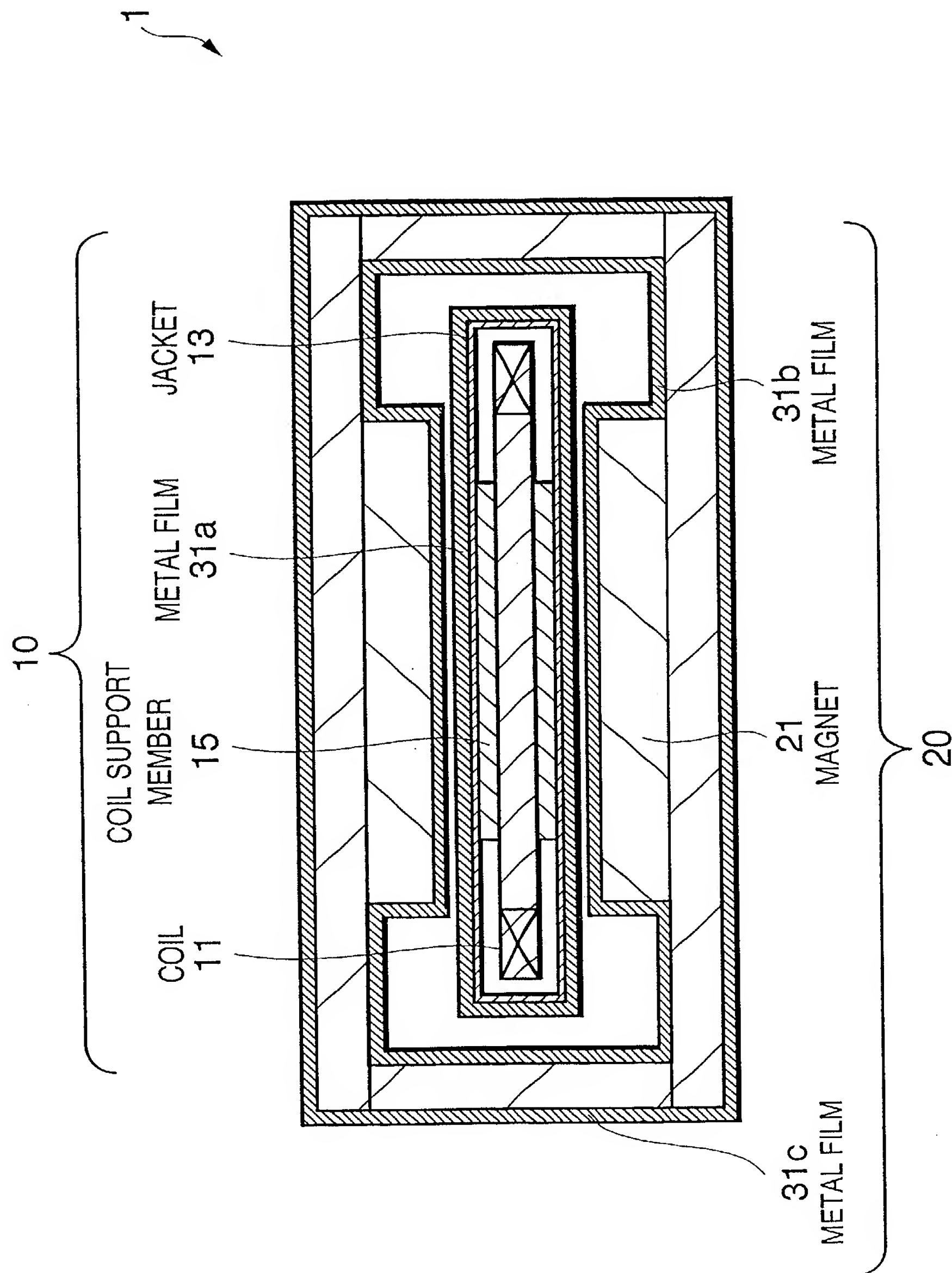


FIG. 2

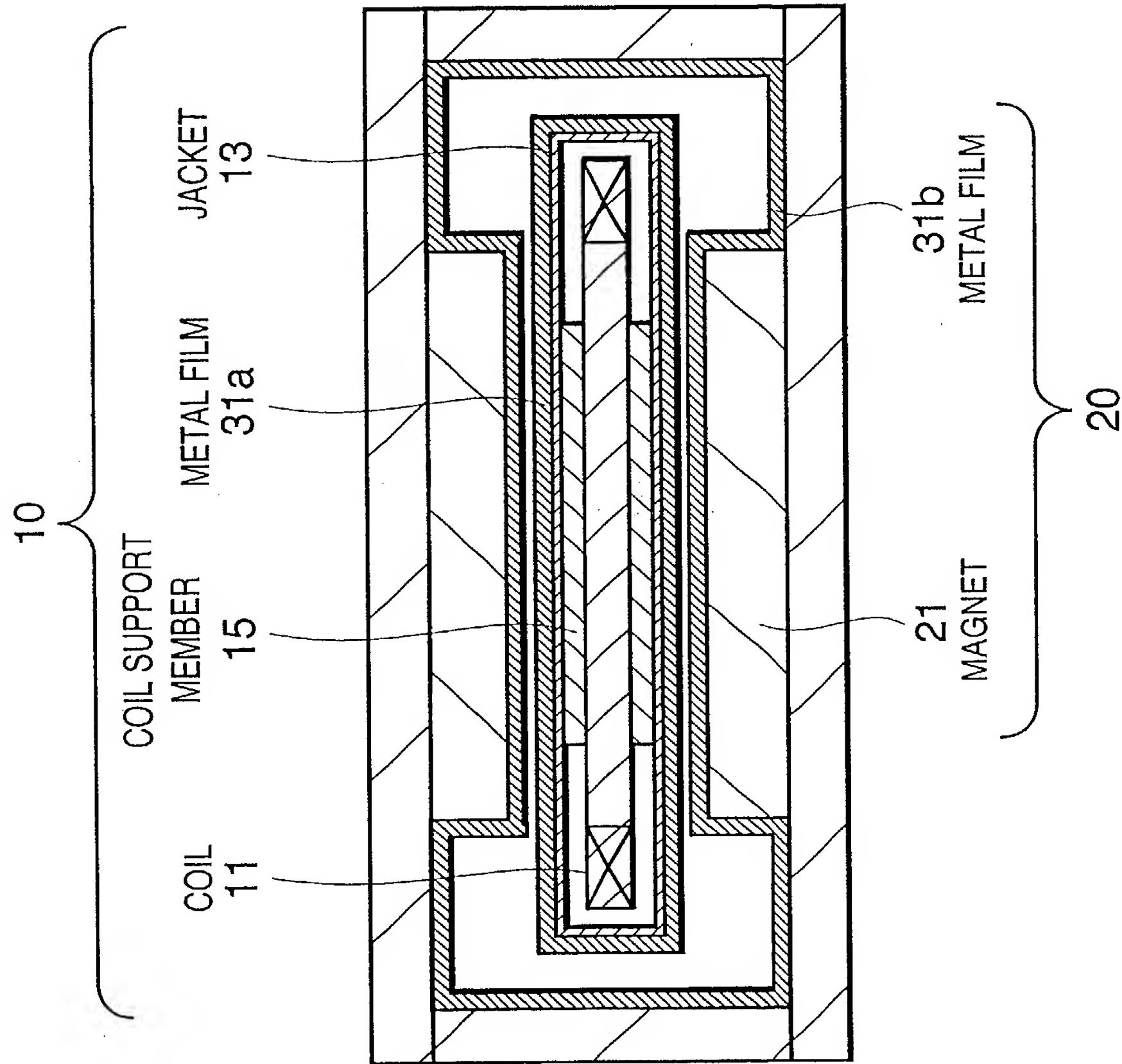


FIG. 3

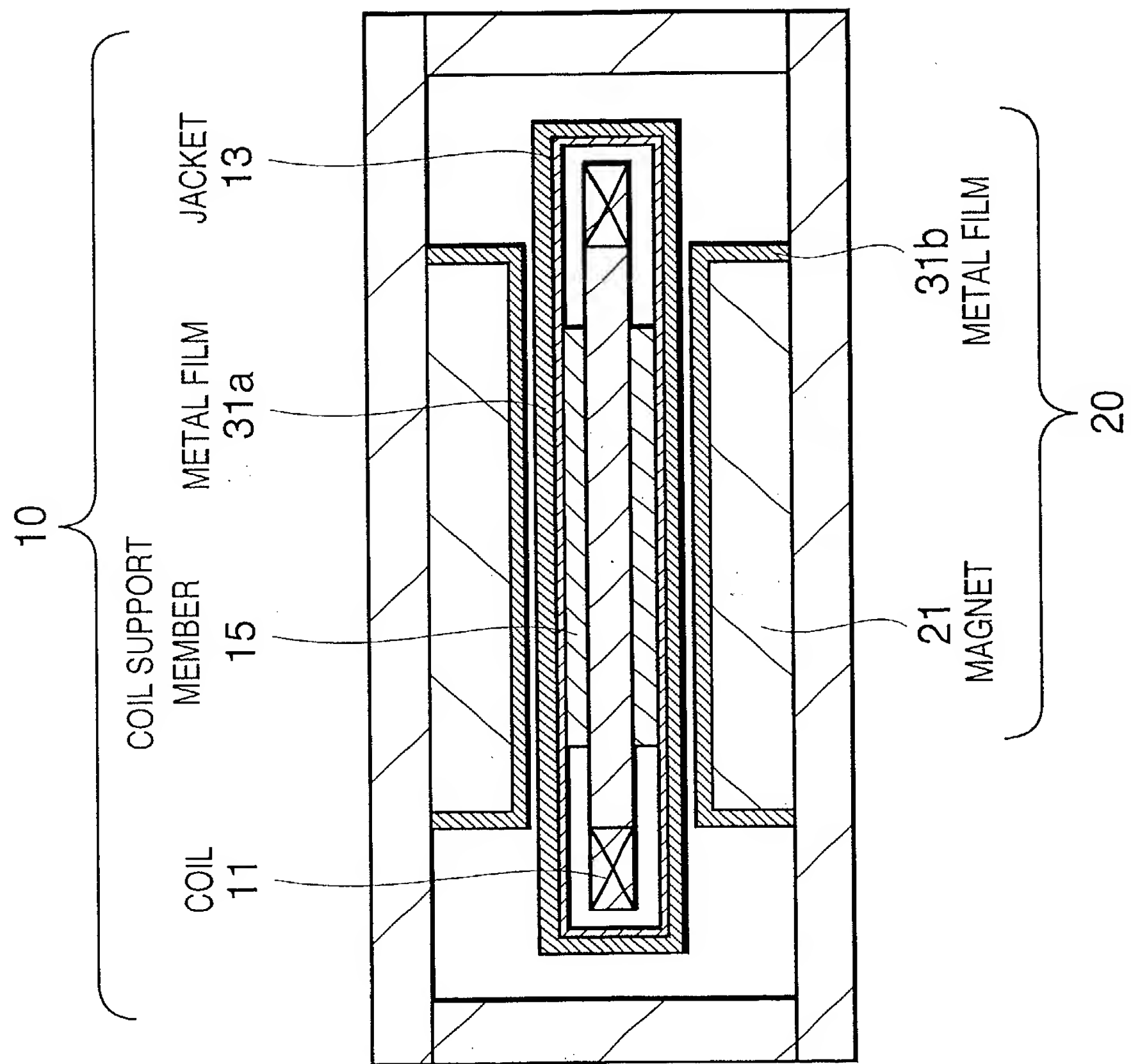


FIG. 4

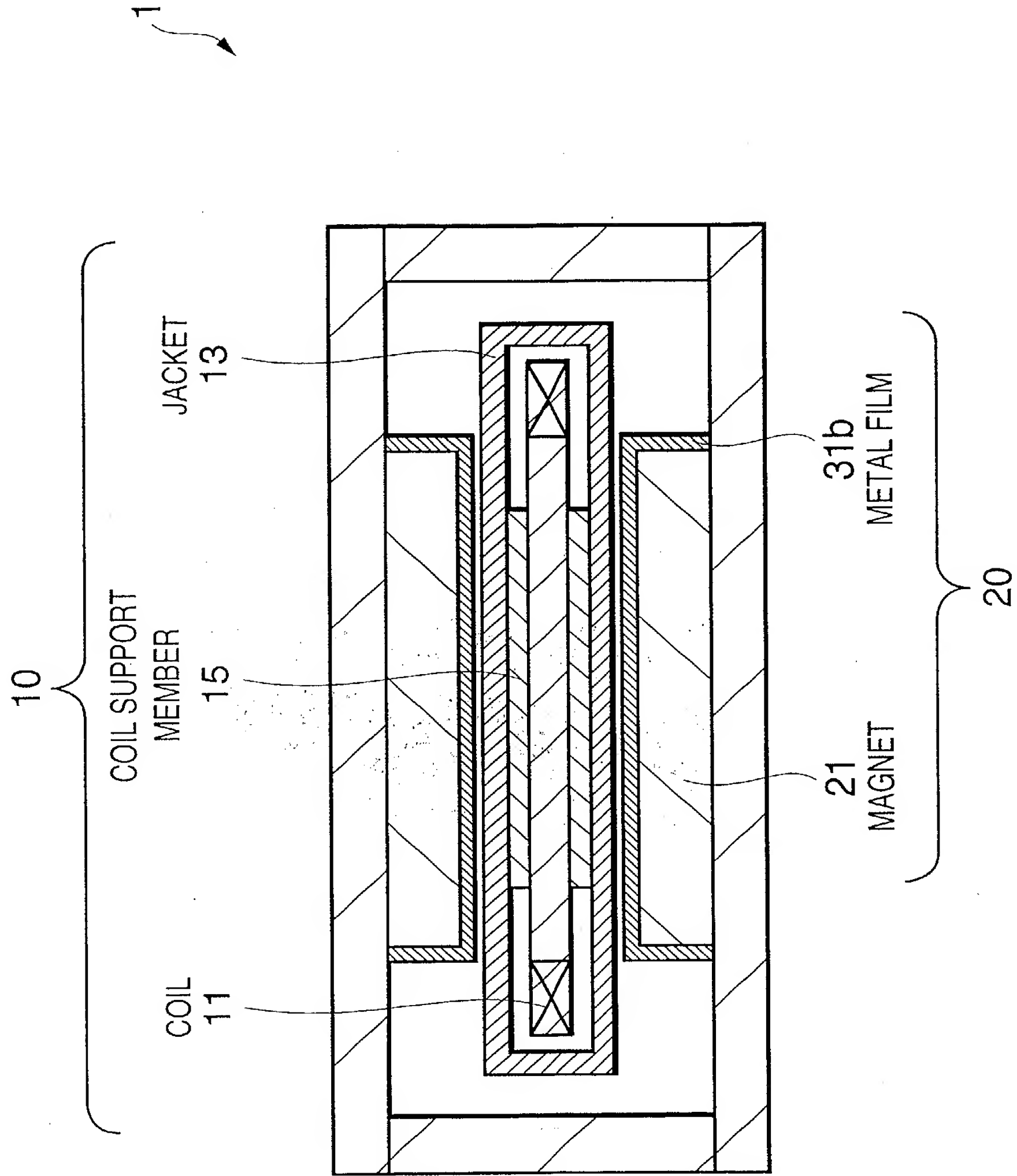


FIG. 5

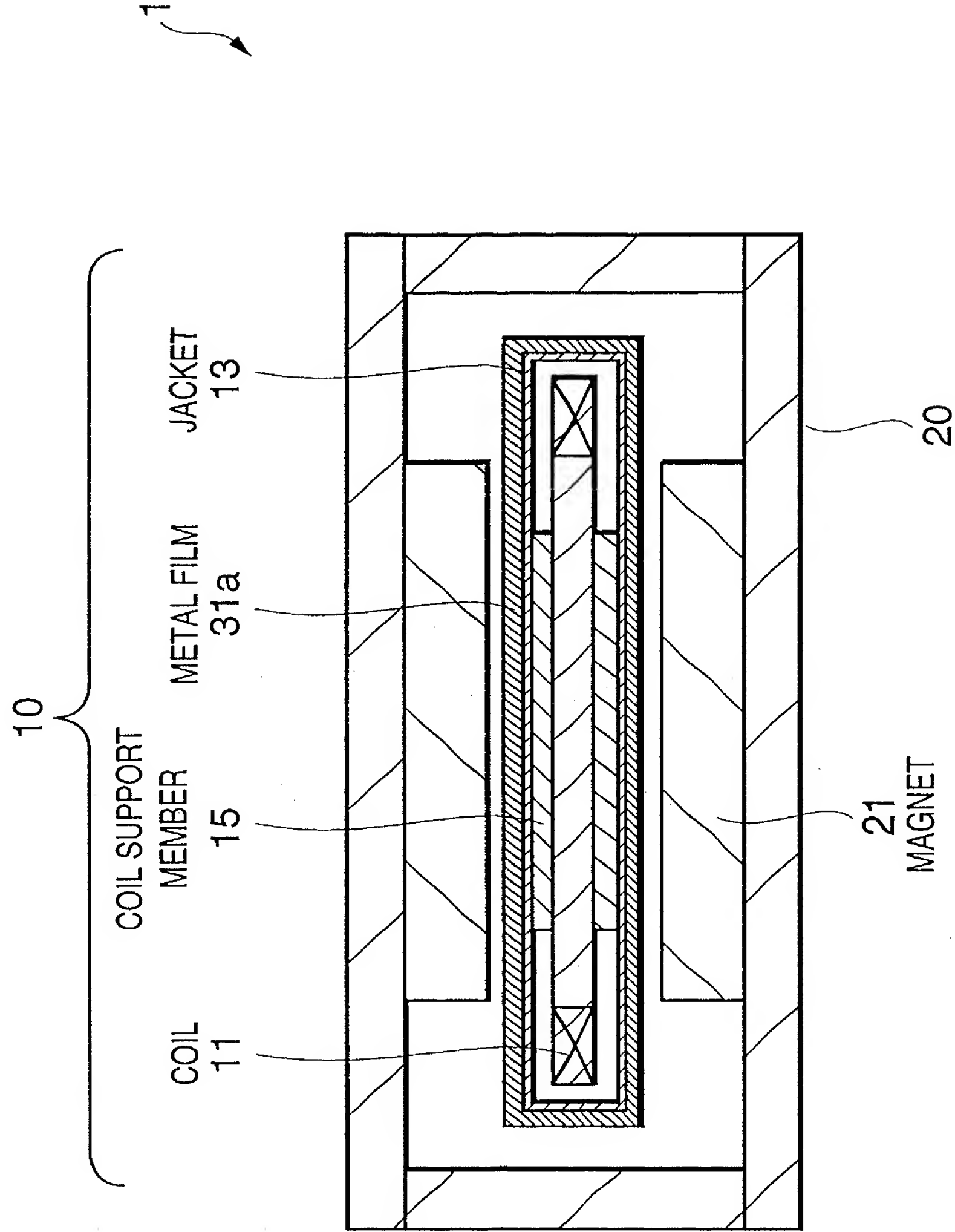
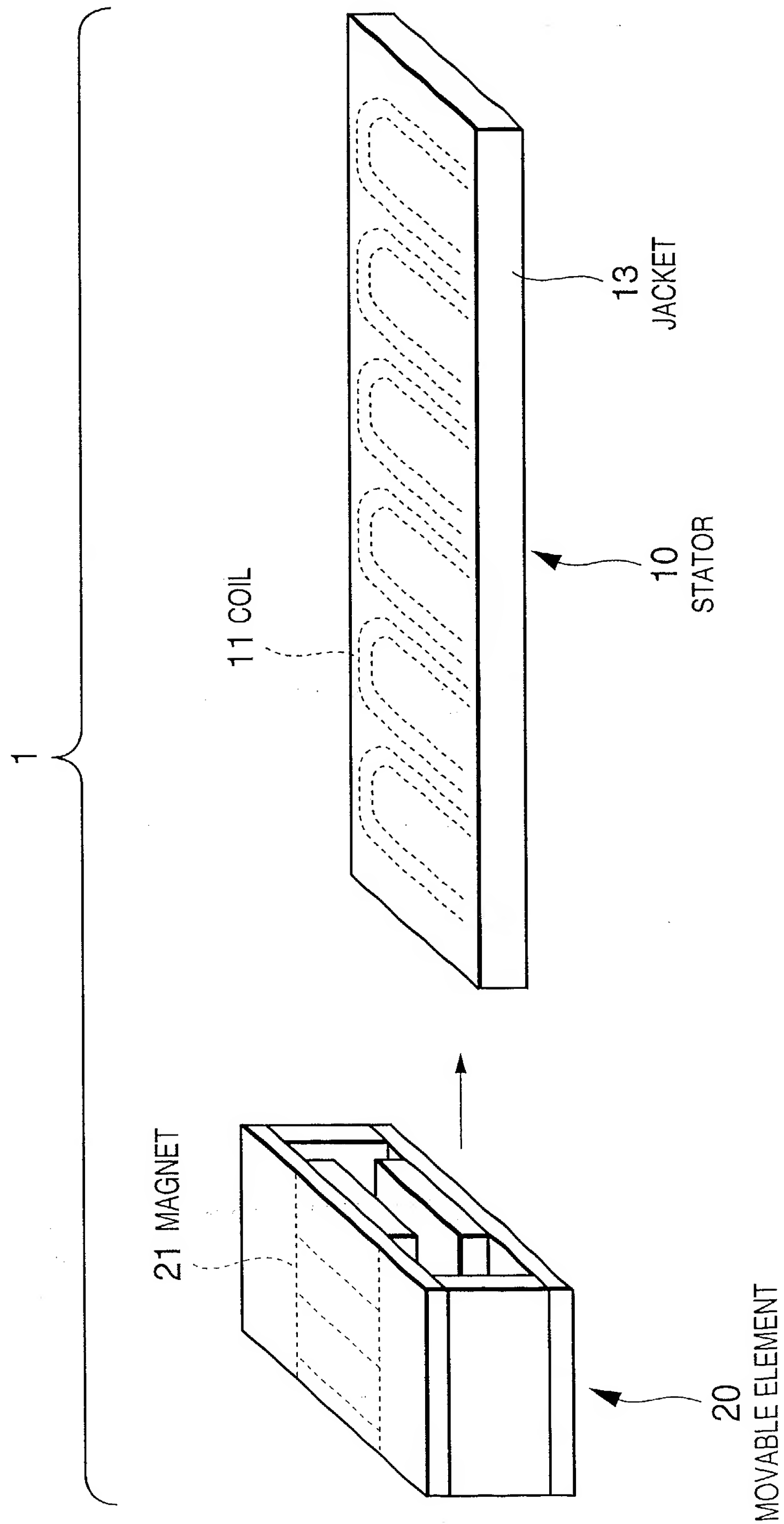


FIG. 6



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FIG. 7A

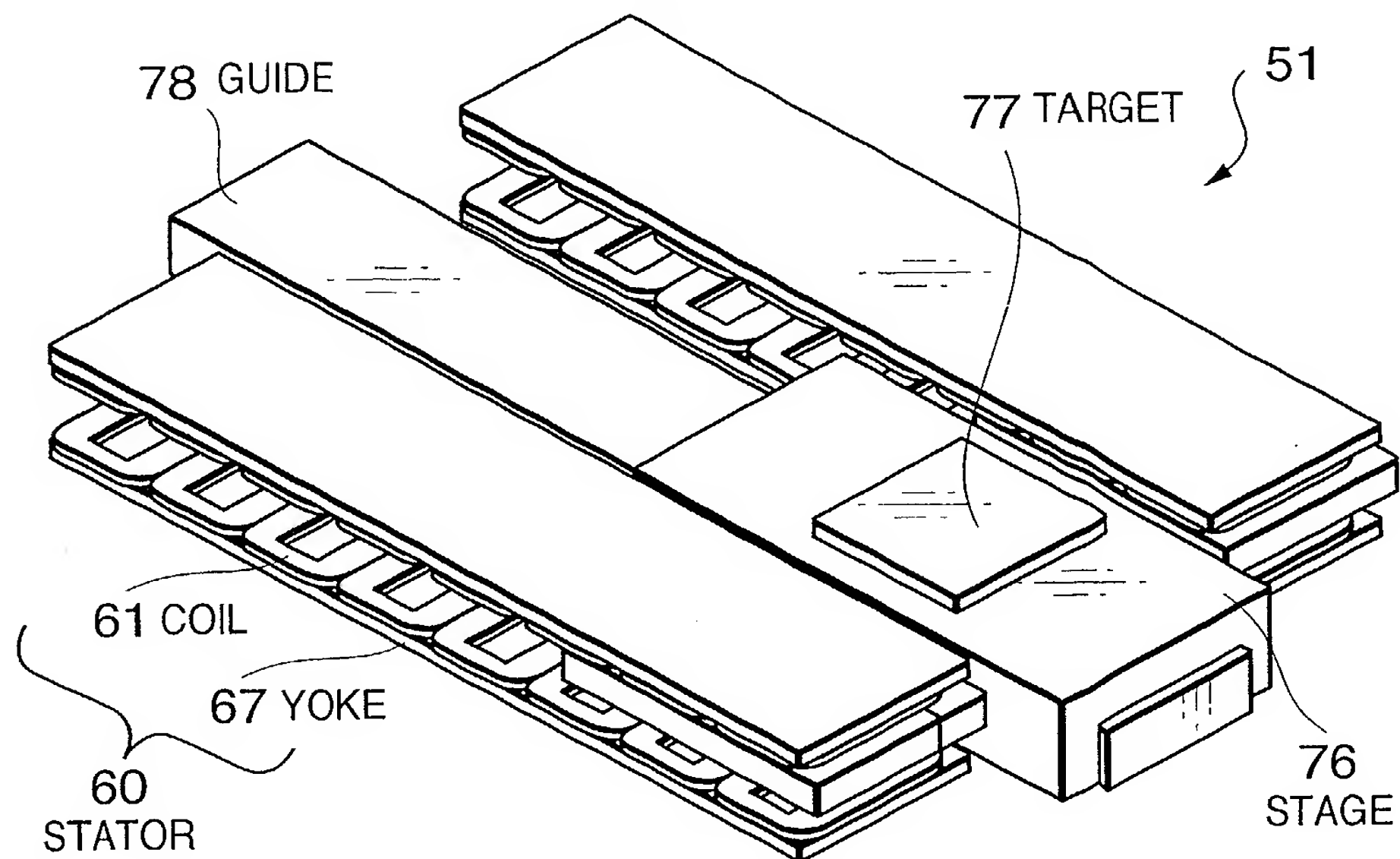
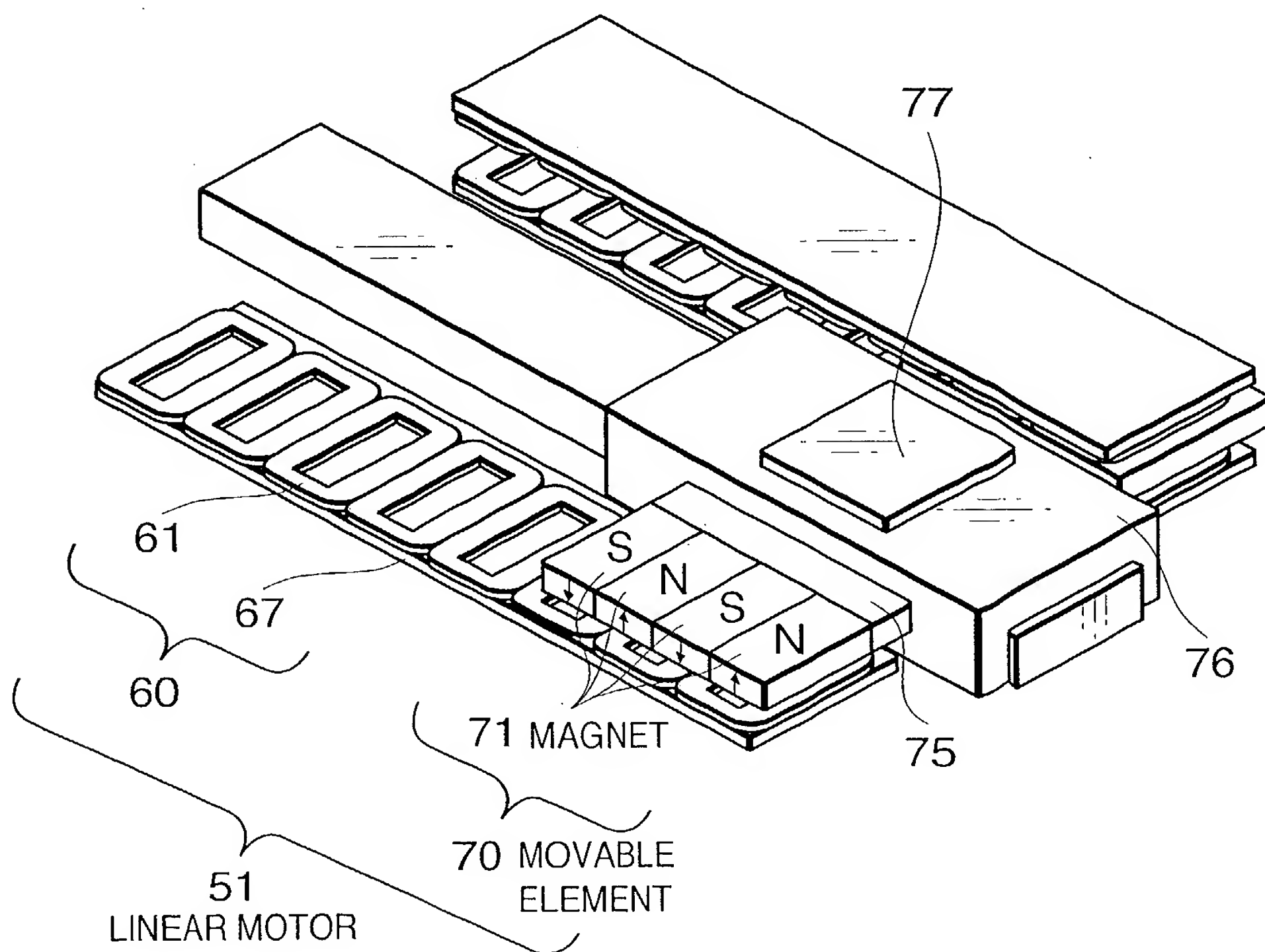
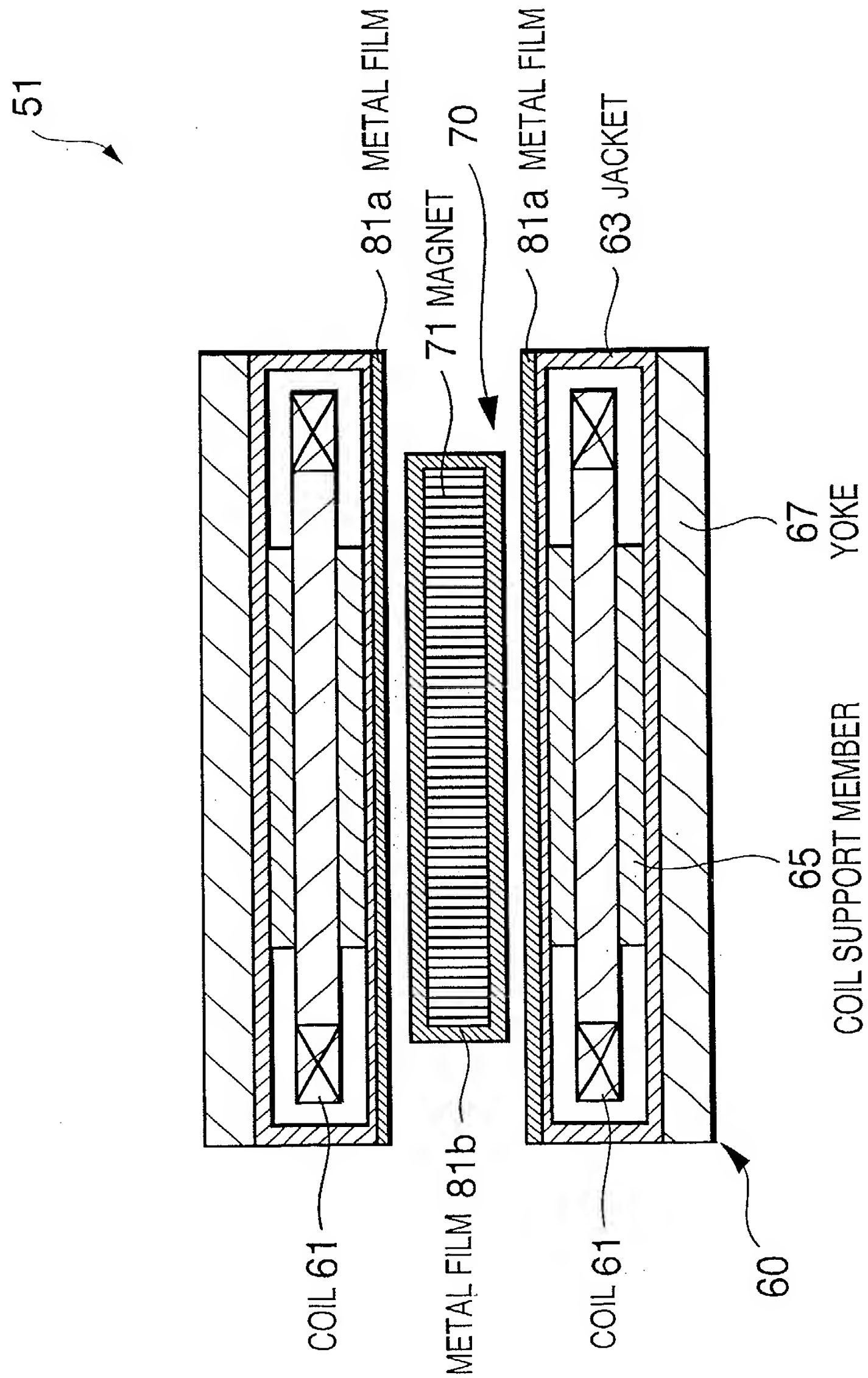


FIG. 7B



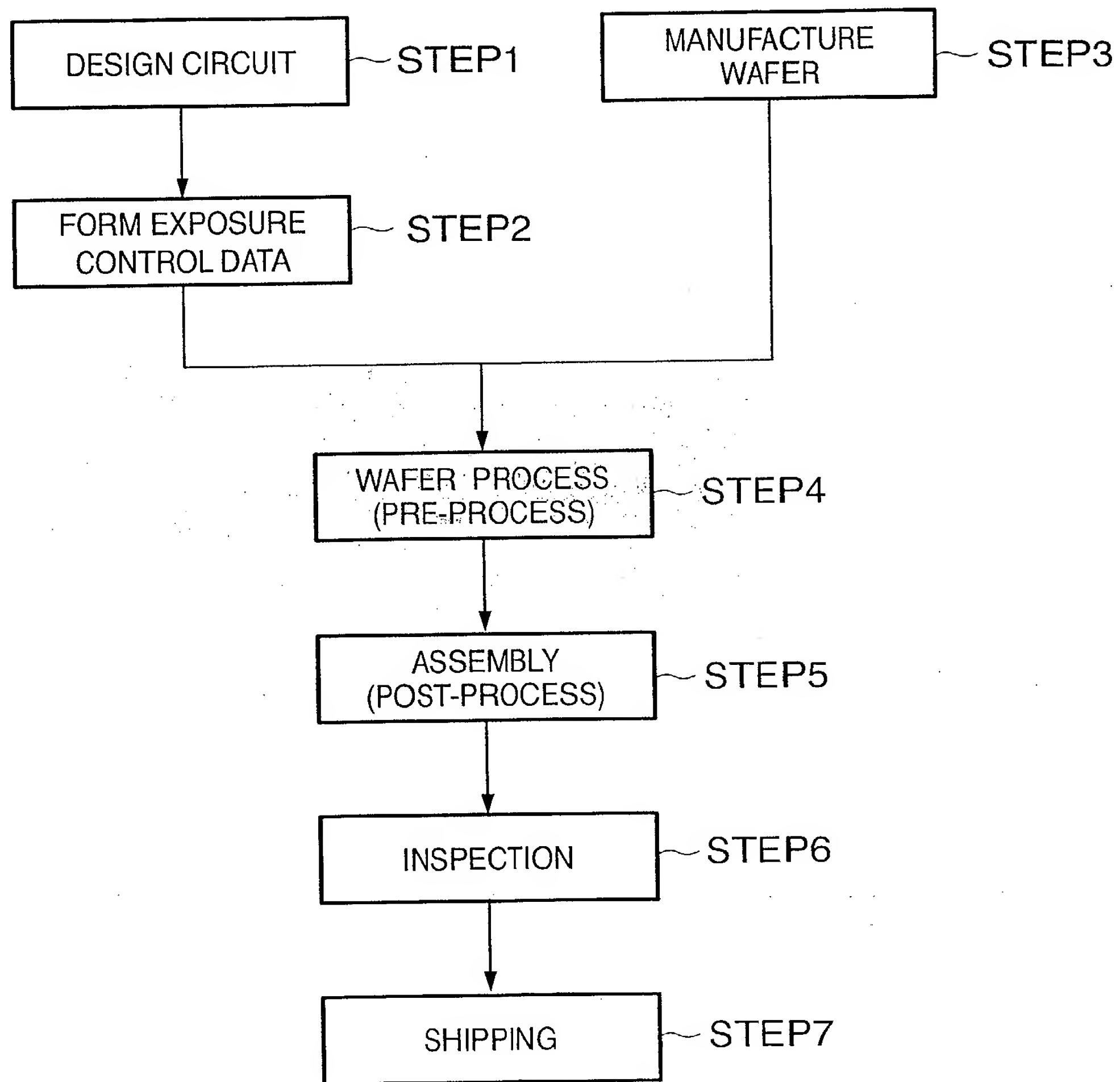
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FIG. 8



[illegible]

FIG. 10



SEMICONDUCTOR DEVICE MANUFACTURING FLOW

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FIG. 11

